

FIG. 1

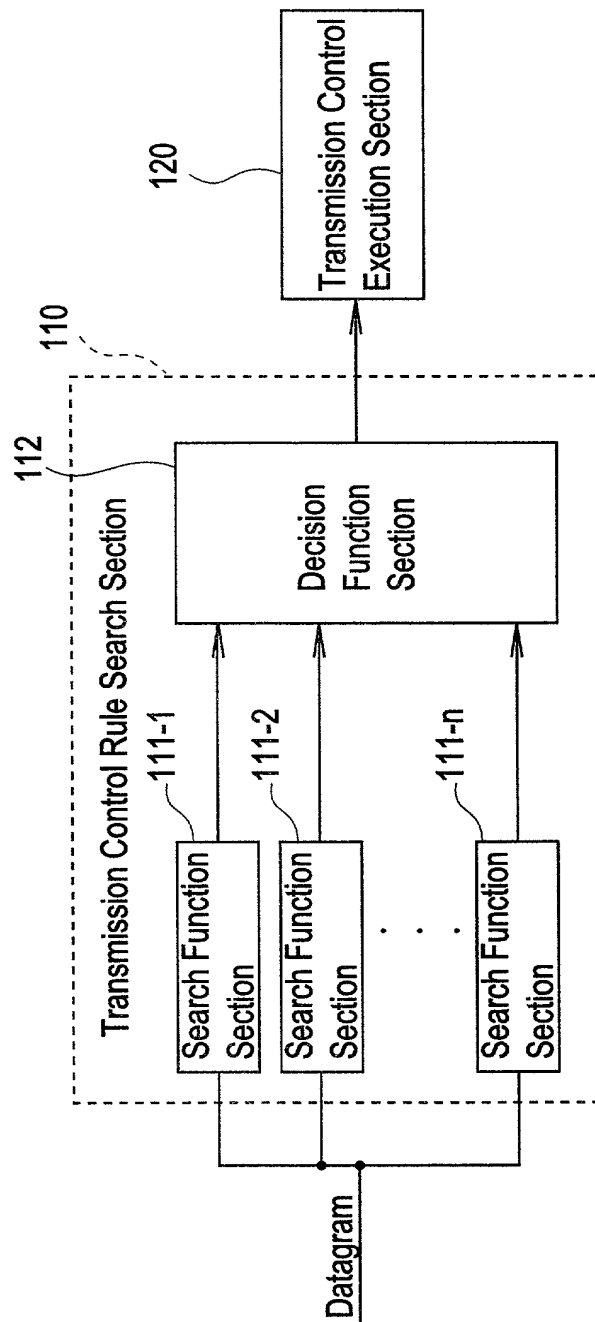
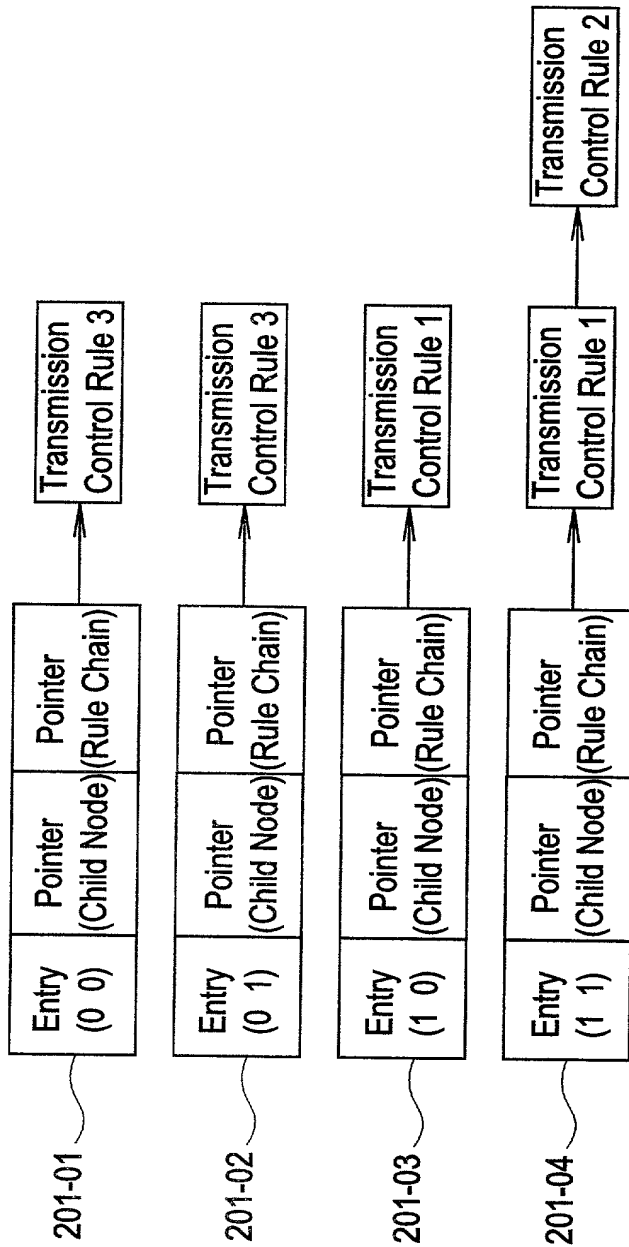


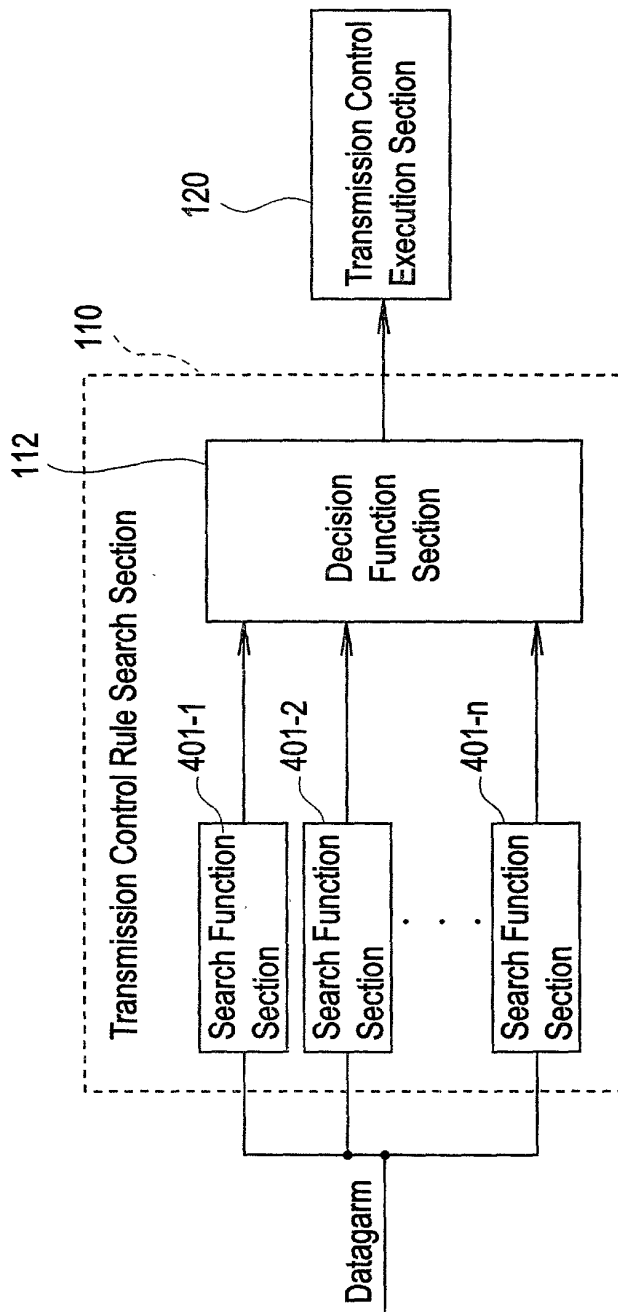
FIG.2



*FIG.3*

Transmission Control Rule 1	Bit Sequence of Entry = 1 *
Transmission Control Rule 2	Bit Sequence of Entry = 11
Transmission Control Rule 3	Bit Sequence of Entry = 0 *

FIG. 4





*FIG. 6*

501-8

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
0	1	0	0	0	0	0	0	0	0	0	0	1	0	1	0

FIG. 7

